

4
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

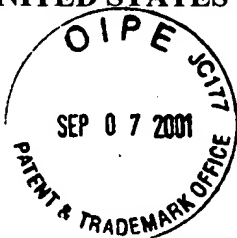
In re application of:

Mark D. Roberts

Appl. No. 09/878,923
Confirmation No. 2854

Filed: June 13, 2001

For: SYSTEM AND METHOD FOR
APPLYING DELAY CODES TO
PULSE TRAIN SIGNALS



Art Unit: 2634

Examiner: (Unassigned)

Atty. Docket No.: 28549-165405

Customer No.:



26694

PATENT TRADEMARK OFFICE

SUBMISSION OF SUBSTITUTE DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Submitted herewith are 12 sheets of formal drawings containing Figures 1-10.

Date: September 7, 2001

Respectfully submitted,

Robert S. Babayi

Registration No. 33,471

VENABLE

P.O. Box 34385

Washington, D.C. 20043-9998

Telephone: (202) 962-4800

Telefax: (202) 962-8300

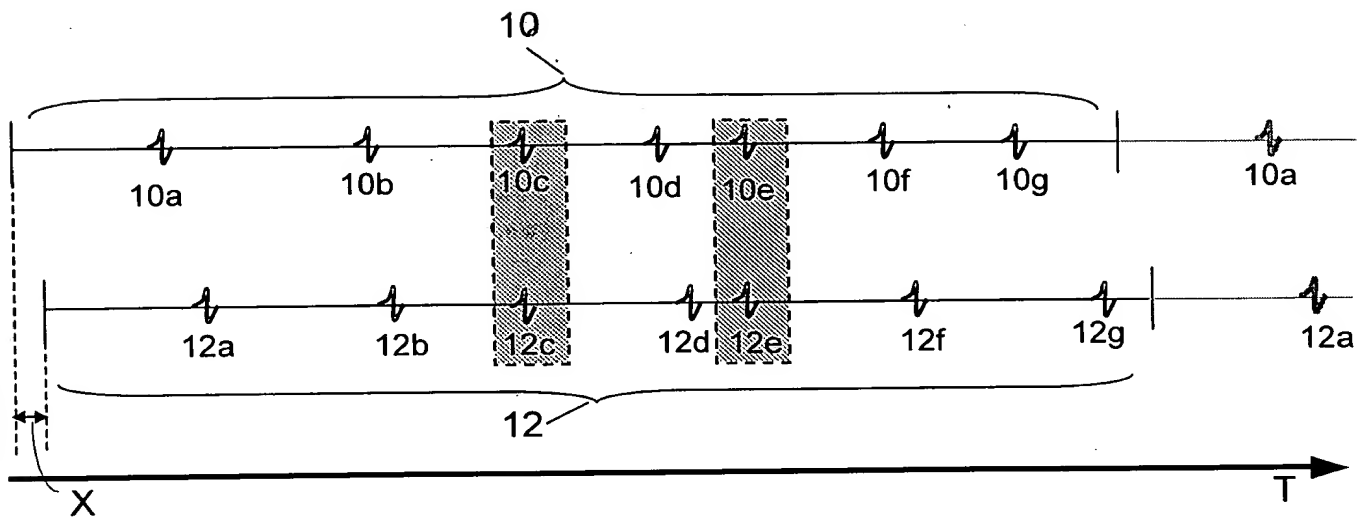


FIG. 1

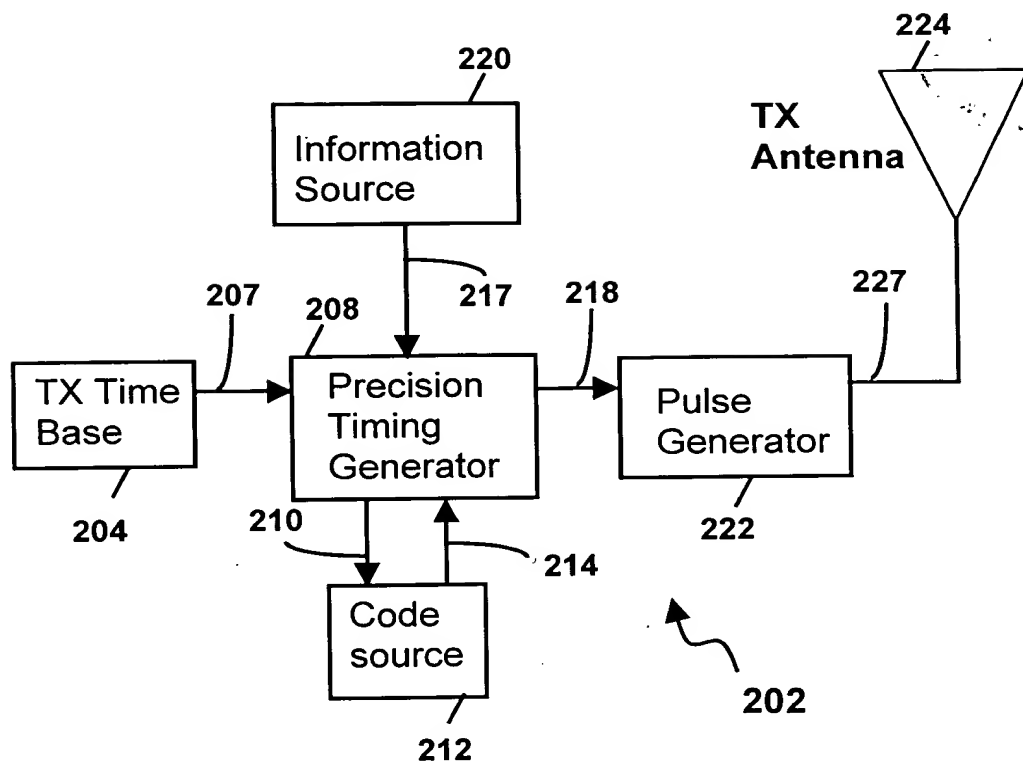


FIG. 2

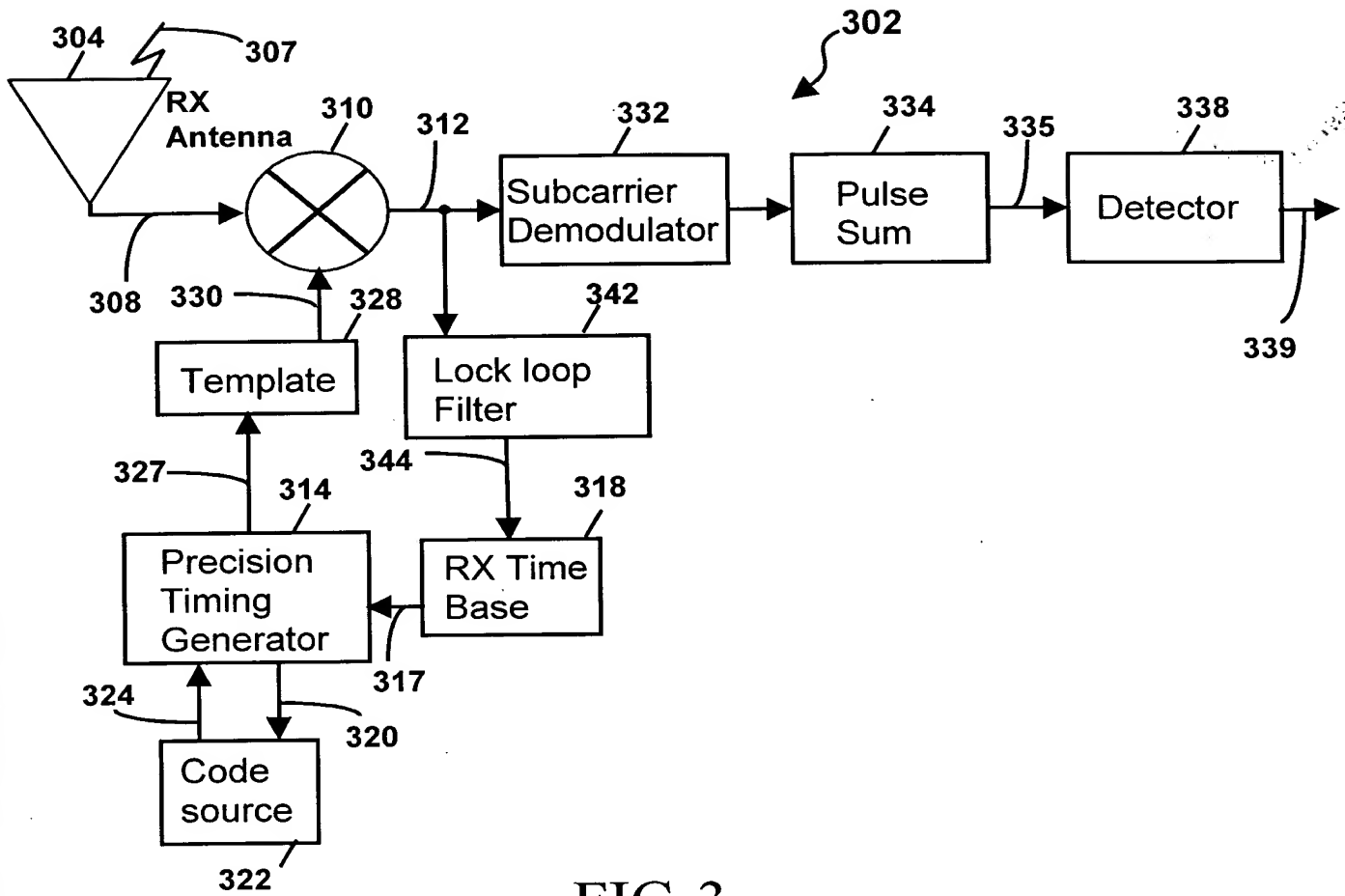
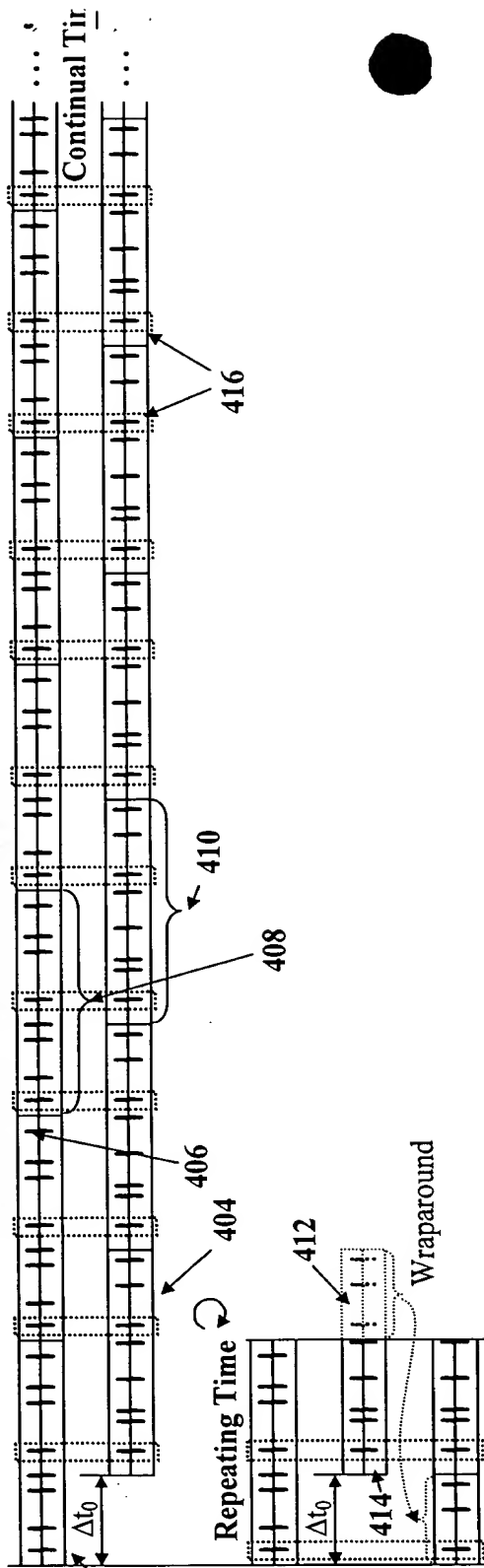


FIG. 3

A schematic diagram of a linear array 14. The array consists of a series of elements, each represented by a vertical line with a horizontal line through its center. The elements are connected in series. Four diodes, labeled D_1 , D_2 , D_3 , and D_4 , are connected in series with the elements. The array is divided into four sections by curved lines: 16a, 16b, 16c, and 16d. Section 16a contains the first element and diode D_1 . Section 16b contains the second element and diode D_2 . Section 16c contains the third element and diode D_3 . Section 16d contains the fourth element and diode D_4 . The array is terminated at both ends by a horizontal line with an arrow pointing to the right.

FIG. 4a

Example 1: Without Delay Code 402



Example 2: With Delay Code 402

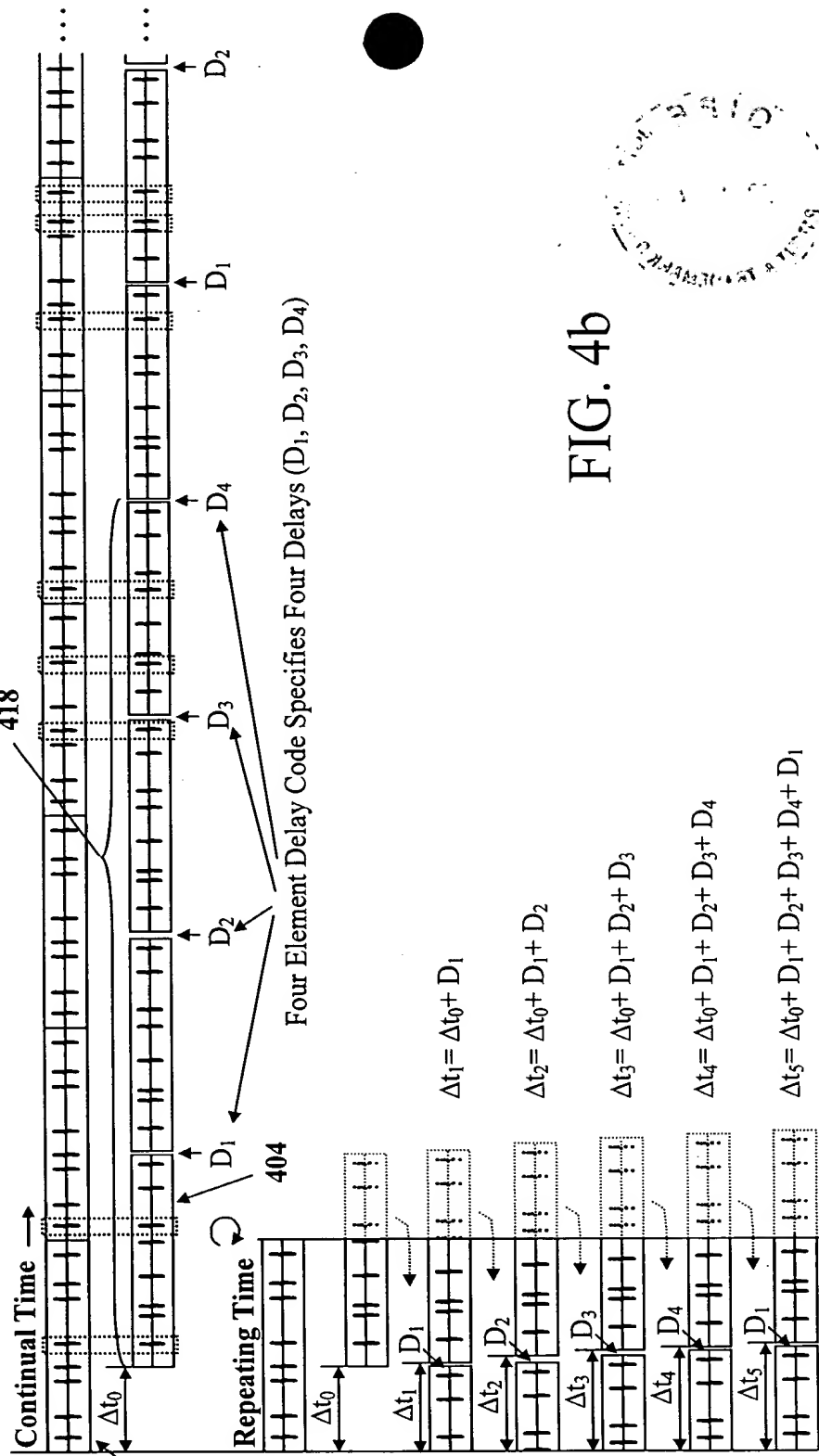


FIG. 4b

-506



FIG. 5

Cross-correlation of 5th and 6th Codes (Time Offsets in 1 Subframe Increments)

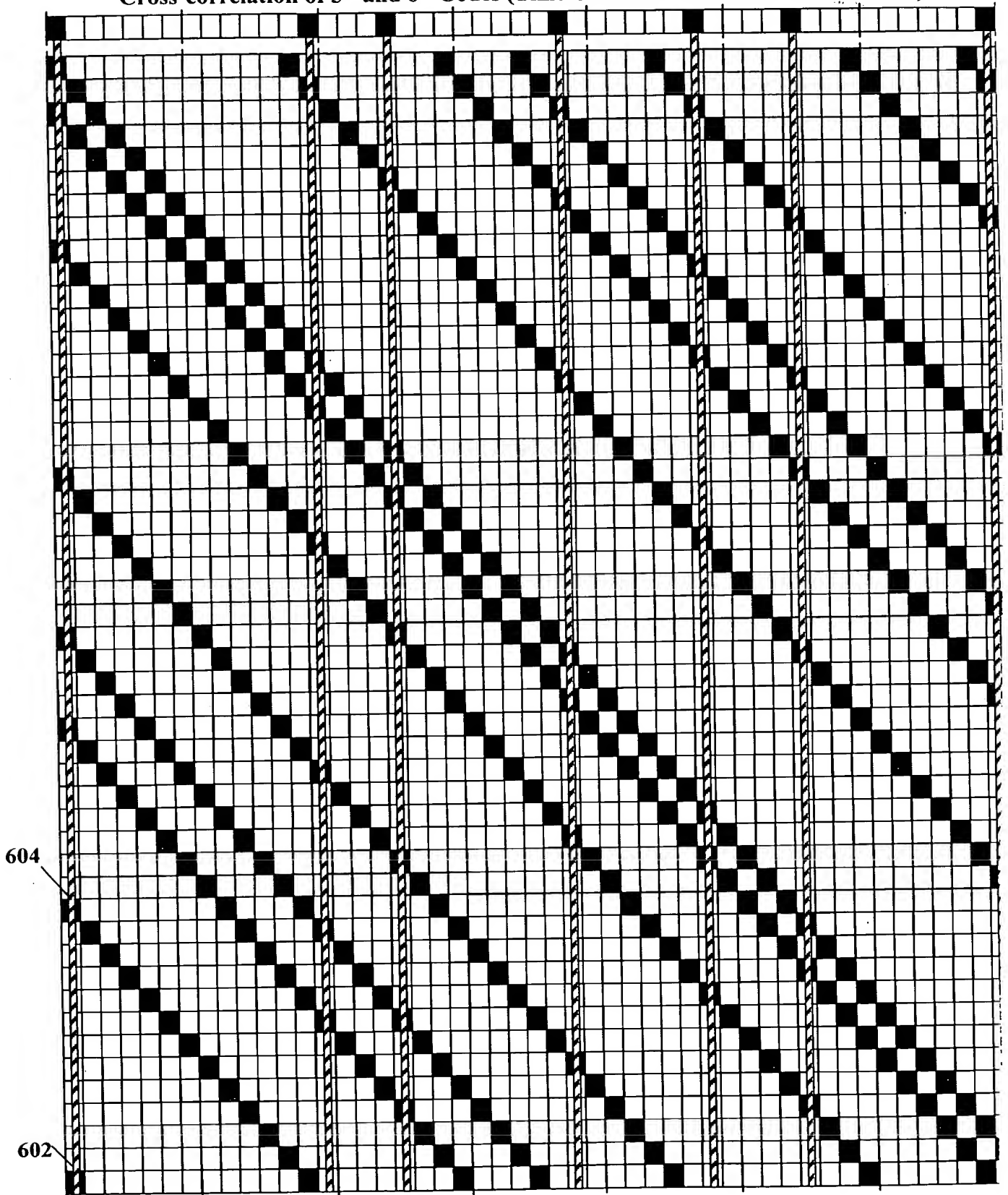


FIG. 6

Cross-correlation of 5th and 6th Codes

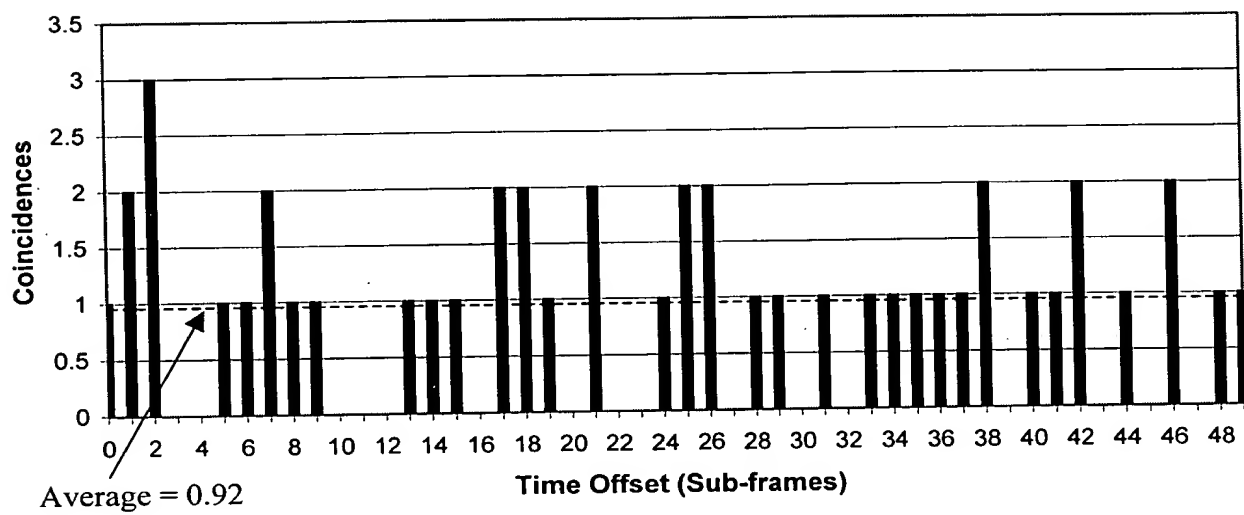


FIG. 7

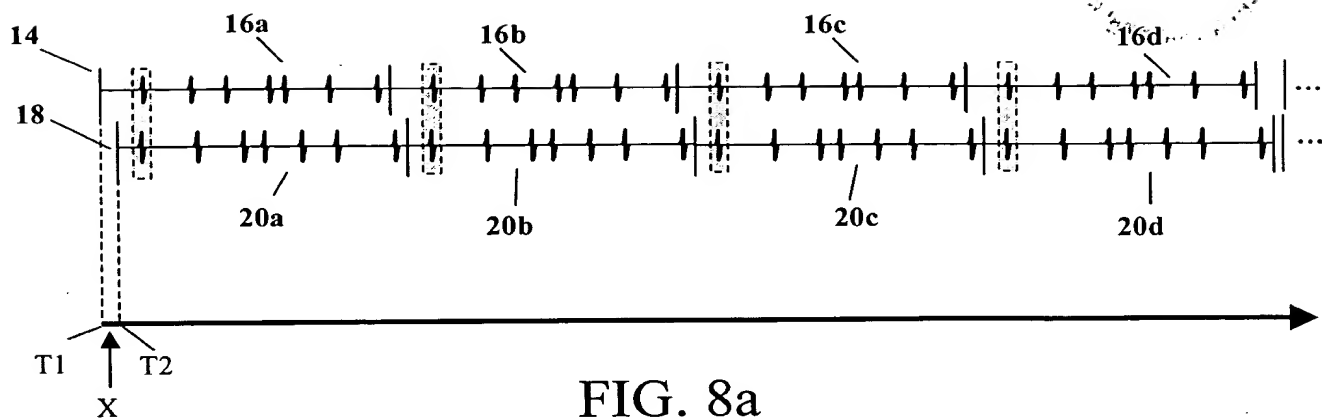


FIG. 8a

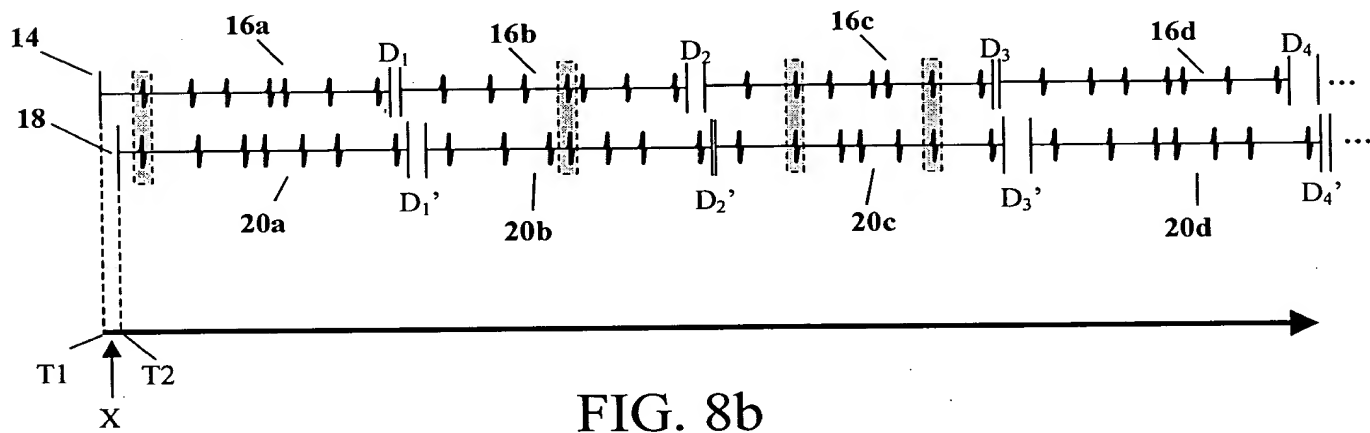


FIG. 8b

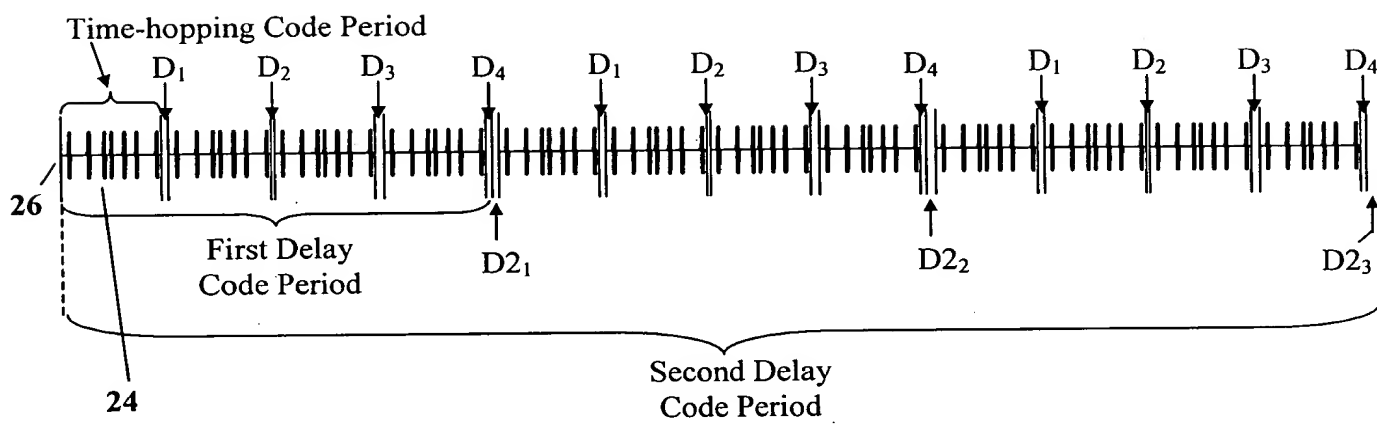


FIG. 9

